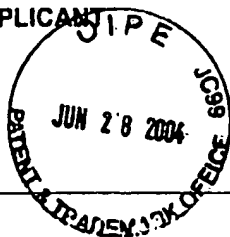


Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)



Complete if Known

Application Number	10/751263
Filing Date	December 30, 2003
First Named Inventor	Kuo, Chen-Chi
Group Art Unit	2186
Examiner Name	Unknown

Sheet 1 of 1

Attorney Docket No: Intel-010PUS

**US PATENT DOCUMENTS**

Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate
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**FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	T <sup>2</sup>
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**OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		"Intel IXP2800 Network Processor For OC-192/10 Gbps network edge and core applications", Product Brief, (2002), pp. 1-6	
		United States Patent Application, "MEMORY INTERLEAVING", Application No. 10/340,220, filed January 10, 2003	

EXAMINER

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Substitute Disclosure Statement Form (PTO-1449)

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